

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2006	delay\$3 near lock\$3 near loop\$3	USPAT	OR	OFF	2004/10/15 18:27
S2	317	coarse near delay	USPAT	OR	OFF	2004/10/08 07:16
S3	493	fine near delay	USPAT	OR	OFF	2004/10/08 07:16
S4	54	S1 and S2 and S3	USPAT	OR	OFF	2004/10/12 17:02
S5	5	((("5,604,775") or ("5,963,069") or ("6,069,506") or ("6,075,415") or ("6,297,680") or ("6.445.231"))).PN.	USPAT; USOCR	OR	OFF	2004/10/08 07:20
S6	6	((("5,109,394") or ("5,355,037") or ("5,744,991") or ("6,049,239") or ("6,101,197") or ("6,130,552"))).PN.	USPAT; USOCR	OR	OFF	2004/10/08 08:55
S7	1207714	digital near\$4 delay near lock\$2 near loop\$3	USPAT	OR	OFF	2004/10/12 13:06
S8	289	S7 and coarse near delay	USPAT	OR	OFF	2004/10/12 13:06
S9	225	S8 and fine near delay	USPAT	OR	OFF	2004/10/12 13:07
S10	58	S9 and phase near detector	USPAT	OR	OFF	2004/10/12 16:28
S12	1	("6,587,534").PN.	USPAT; USOCR	OR	OFF	2004/10/12 16:31
S13	1207714	digital near\$4 delay near lock\$2 near loop\$3	USPAT	OR	OFF	2004/10/12 16:58
S14	289	S13 and coarse near delay	USPAT	OR	OFF	2004/10/12 16:58
S15	225	S14 and fine near delay	USPAT	OR	OFF	2004/10/12 16:58
S16	83	S15 and shift near register	USPAT	OR	OFF	2004/10/12 16:58
S17	2015	delay\$3 near lock\$3 near loop\$3	USPAT	OR	OFF	2004/10/12 17:02
S18	318	coarse near delay	USPAT	OR	OFF	2004/10/12 17:02
S19	495	fine near delay	USPAT	OR	OFF	2004/10/12 17:02
S20	55	S17 and S18 and S19	USPAT	OR	OFF	2004/10/12 17:02
S21	2966	(internal and external) near clock	USPAT	OR	OFF	2004/10/12 17:16
S22	1937	S21 and S13	USPAT	OR	OFF	2004/10/12 17:16
S23	24	S22 and S18 and S19	USPAT	OR	OFF	2004/10/12 17:16
S28	14800	overshoot\$2	USPAT	OR	OFF	2004/10/12 17:40
S29	3065	undershoot\$2	USPAT	OR	OFF	2004/10/12 17:40
S30	1649	S13 and S28 and S29	USPAT	OR	OFF	2004/10/12 17:41
S31	1	S30 and S18 and S19	USPAT	OR	OFF	2004/10/12 17:43
S32	808835	delay lock loop	USPAT	OR	OFF	2004/10/12 17:43
S33	1	S32 and S28 and S29 and S18 and S19	USPAT	OR	OFF	2004/10/12 17:43
S34	1526	S32 and S28 and S29	USPAT	OR	OFF	2004/10/12 17:44
S35	56	S34 and coarse and fine	USPAT	OR	OFF	2004/10/12 17:44
S36	106403	synchronization	USPAT	OR	OFF	2004/10/12 17:50

S37	7236	external near clock	USPAT	OR	OFF	2004/10/12 17:50
S38	11537	internal near clock	USPAT	OR	OFF	2004/10/12 17:50
S39	1409	S36 and S37 and S38	USPAT	OR	OFF	2004/10/12 17:51
S40	9	S39 and S28 and S29	USPAT	OR	OFF	2004/10/12 17:51
S41	72940	S13 and S36	USPAT	OR	OFF	2004/10/12 17:52
S42	997	S41 and S37 and S38	USPAT	OR	OFF	2004/10/12 17:52
S43	7	S42 and S28 and S29	USPAT	OR	OFF	2004/10/12 17:54
S44	7654	S36 near clock\$3	USPAT	OR	OFF	2004/10/12 17:54
S45	5631	S44 and S13	USPAT	OR	OFF	2004/10/12 17:54
S46	13	S45 and S18 and S19	USPAT	OR	OFF	2004/10/12 18:00
S48	2015	delay\$3 near lock\$3 near loop\$3	USPAT	OR	OFF	2004/10/13 10:00
S49	318	coarse near delay	USPAT	OR	OFF	2004/10/13 10:00
S50	495	fine near delay	USPAT	OR	OFF	2004/10/13 10:00
S51	14884	phase near detector\$2	USPAT	OR	OFF	2004/10/13 10:00
S52	1243	register near controller\$2	USPAT	OR	OFF	2004/10/13 10:00
S53	0	S48 and S49 and S50 and S51 and S52	USPAT	OR	OFF	2004/10/13 10:00
S54	14800	overshoot\$2	USPAT	OR	OFF	2004/10/13 10:00
S55	3065	undershoot\$2	USPAT	OR	OFF	2004/10/13 10:00
S56	0	S48 and S49 and S50 and S54 and S55	USPAT	OR	OFF	2004/10/13 10:01
S57	2	S48 and S49 and S50 and S54	USPAT	OR	OFF	2004/10/13 10:01
S58	2966	(internal and external) near clock	USPAT	OR	OFF	2004/10/13 10:05
S59	20	S48 and S49 and S50 and S58	USPAT	OR	OFF	2004/10/13 10:05
S60	54	delay near lock near loop and coarse and fine	USPAT	OR	OFF	2004/10/13 17:15
S61	10	adjust near delay and S60	USPAT	OR	OFF	2004/10/13 13:39
S62	49	(delay near lock\$3 near loop) and coarse near delay and fine near delay	USPAT	OR	OFF	2004/10/13 17:15
S63	11	S62 and adjust near delay	USPAT	OR	OFF	2004/10/13 16:16
S64	36	S62 and memory	USPAT	OR	OFF	2004/10/13 17:31
S65	0	("9585035").PN.	USPAT; USOCR	OR	OFF	2004/10/14 16:24
S66	2	delay near lock near loop and coarse near delay and fine near delay and(external and internal) near clock	USPAT	OR	OFF	2004/10/14 16:36
S67	5	delay near lock near loop and coarse near delay and fine near delay and(external and internal)	USPAT	OR	OFF	2004/10/14 16:52

S71	14	delay near lock near loop and (coarse and fine) near delay	USPAT	OR	OFF	2004/10/14 16:53
S72	11	S71 and digital	USPAT	OR	OFF	2004/10/14 16:53
S73	1	("4121159").PN.	USPAT; USOCR	OR	OFF	2004/10/15 15:58
S74	1	("5706057").PN.	USPAT; USOCR	OR	OFF	2004/10/15 16:00
S75	1	("5744991").PN.	USPAT; USOCR	OR	OFF	2004/10/15 16:06
S76	6	select\$2 and (digital near delay near lock near loop) and coarse and fine	USPAT	OR	OFF	2004/10/15 16:07
S77	4	selector and (digital near delay near lock near loop) and coarse and fine	USPAT	OR	OFF	2004/10/15 16:07
S79	111	digital near delay near locked near loop	USPAT	OR	OFF	2004/10/15 16:20
S80	8	S79 and (coarse and fine) near delay	USPAT	OR	OFF	2004/10/15 17:53
S82	2	S80 and (selector or mux or multiplexor)	USPAT	OR	OFF	2004/10/15 16:22
S83	5	multiplexor and S79	USPAT	OR	OFF	2004/10/15 16:28
S84	111	digital near delay near locked near loop	USPAT	OR	OFF	2004/10/15 17:53
S85	62	S84 and memory	USPAT	OR	OFF	2004/10/15 18:11
S86	3	S85 and (coarse and fine) near delay	USPAT	OR	OFF	2004/10/15 17:57
S87	7	S84 and memory near cell\$2	USPAT	OR	OFF	2004/10/15 17:57
S88	0	S84 and memory near block	USPAT	OR	OFF	2004/10/15 18:11
S89	31	S84 and memory near device	USPAT	OR	OFF	2004/10/15 18:12
S90	2	S89 and (coarse and fine) near delay	USPAT	OR	OFF	2004/10/15 18:13
S91	2	S89 and (coarse and fine) near loop	USPAT	OR	OFF	2004/10/15 18:13
S92	0	S89 and (coarse and fine) near segment	USPAT	OR	OFF	2004/10/15 18:13
S93	0	((("9649192") or ("60151446"))).PN.	USPAT; USOCR	OR	OFF	2004/10/15 18:29
S94	0	("9649192").PN.	USPAT; USOCR	OR	OFF	2004/10/15 18:30
S95	1	("6388480").PN.	USPAT; USOCR	OR	OFF	2004/10/15 18:31
S96	1	("6151446").PN.	USPAT; USOCR	OR	OFF	2004/10/15 18:31
S97	0	("60151446").PN.	USPAT; USOCR	OR	OFF	2004/10/15 18:31

S98	83721	clock near signal	USPAT	OR	OFF	2004/10/18 12:57
S99	4048	method near generat\$3 and S98	USPAT	OR	OFF	2004/10/18 12:57
S100	27	S99 and (fine and coarse) near delay	USPAT	OR	OFF	2004/10/18 13:04
S105	289	S104 and coarse near delay	USPAT	OR	OFF	2004/10/19 16:42
S106	225	S105 and fine near delay	USPAT	OR	OFF	2004/10/19 16:42
S108	289	S107 and coarse near delay	USPAT	OR	OFF	2004/10/19 16:42
S109	225	S108 and fine near delay	USPAT	OR	OFF	2004/10/19 16:42
S141	111	digital near delay near locked near loop	USPAT	OR	OFF	2004/10/19 16:42
S142	8	S141 and (coarse and fine) near delay	USPAT	OR	OFF	2004/10/19 16:42
S144	111	digital near delay near locked near loop	USPAT	OR	OFF	2004/10/19 16:42
S145	62	S144 and memory	USPAT	OR	OFF	2004/10/19 16:42
S146	31	S144 and memory near device	USPAT	OR	OFF	2004/10/19 16:42
S147	7046	dll and selector and phase detector and coarse and fine	USPAT	OR	OFF	2004/10/27 17:10
S148	2032	delay\$3 near lock\$3 near loop\$3	USPAT	OR	OFF	2004/10/27 17:11
S149	318	coarse near delay	USPAT	OR	OFF	2004/10/27 17:11
S150	496	fine near delay	USPAT	OR	OFF	2004/10/27 17:11
S151	9	S148 and S149 and S150 and selector	USPAT	OR	OFF	2004/10/27 17:11